

REPLY UNDER 37 CFR 1.116  
EXPEDITED PROCEDURE  
TECHNOLOGY CENTER 1700  
Docket No. 0091.1032

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Ji Yong PARK et al.

Application No. 10/690,507

Group Art Unit: 1792

Confirmation No. 6043

Filed: October 23, 2003

Examiner: Matthew J. Song

For: METHOD FOR MANUFACTURING POLYCRYSTALLINE SILICON THIN FILM AND  
THIN FILM TRANSISTOR FABRICATED USING POLYCRYSTALLINE SILICON THIN  
FILM MANUFACTURED BY THE MANUFACTURING METHOD

**NOTICE OF APPEAL FROM THE EXAMINER TO  
THE BOARD OF PATENT APPEALS AND INTERFERENCES**

**Mail Stop AF**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

The applicants hereby appeal to the Board of Patent Appeals and Interferences from the decision of the Primary Examiner mailed January 26, 2009, finally rejecting claims 1, 3, 6, 7, 10 and 13.

The items checked below are appropriate:

1. PETITION FOR EXTENSION OF TIME AND FEE

— Attached is a petition for a -month extension of time  
for reply to the final rejection. \$0.00

2. APPEAL FEE

X Other than a small entity \$540.00

TOTAL FEE \$540.00

3. PAYMENT

X Credit card payment via EFS.

X Charge Deposit Account No. 503333 for any fee deficiency.

4. PRE-APPEAL BRIEF REQUEST FOR REVIEW

X Enclosed is a Pre-Appeal Brief Request for Review pursuant to 1296 OG 67 and 1303 OG 21.

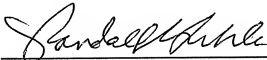
Respectfully submitted,

STEIN, MCEWEN & BUI, LLP

Date:

03/26/09

By:



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